Page 1 of 15 Xinghao Chen, et al. FIS920010060US1

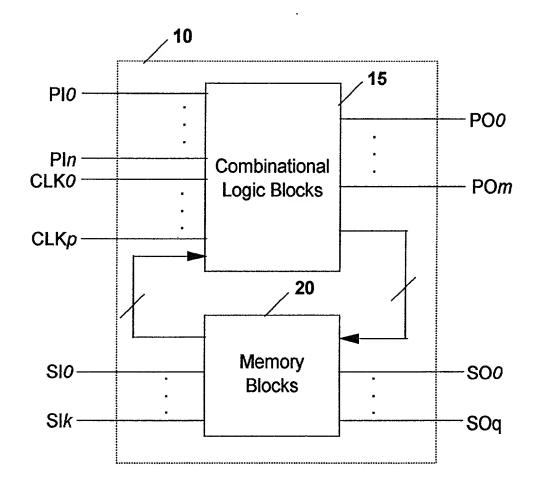


Figure 1.

Page 2 of 15 Xinghao Chen, et al. FIS920010060US1

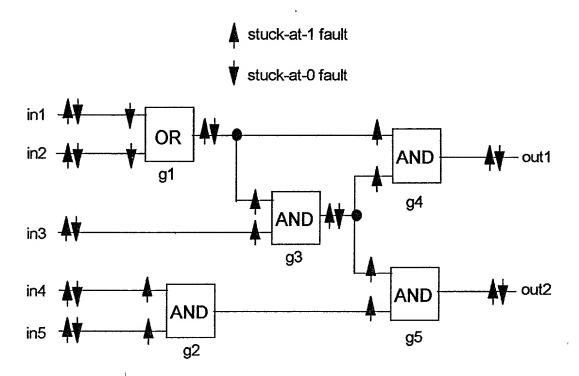


Figure 2..

### Page 3 of 15 Xinghao Chen, et al. FIS920010060US1

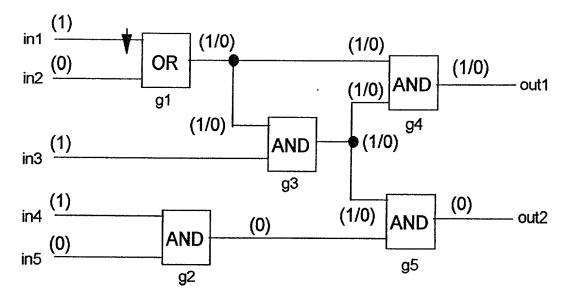


Figure 3.

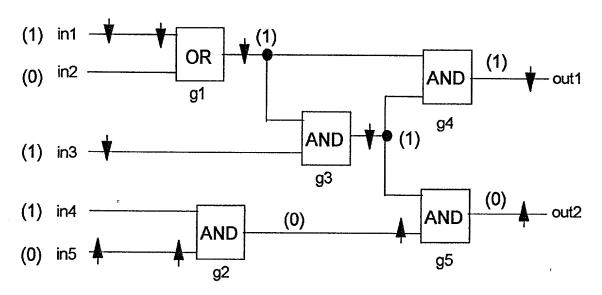


Figure 4.

## Page 4 of 15 Xinghao Chen, et al. FIS920010060US1

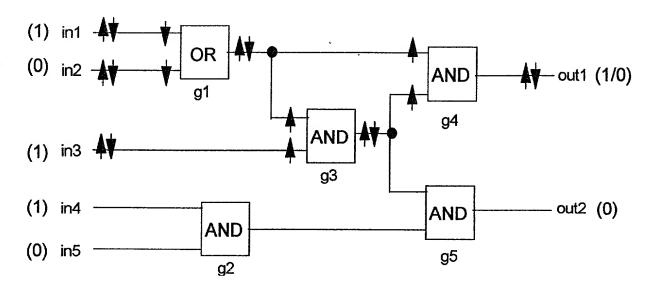


Figure 5

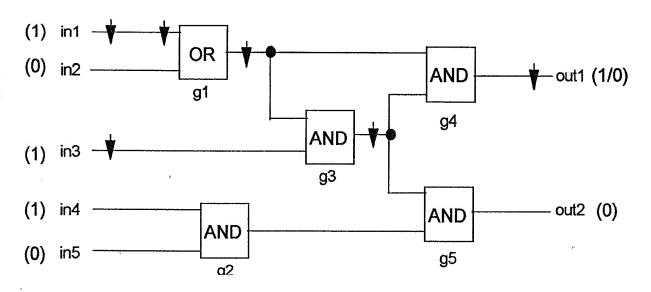
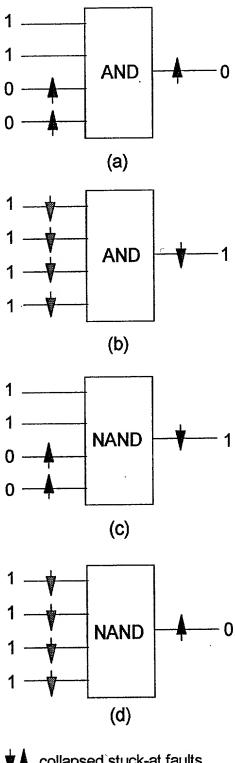


Figure 6.

Page > 01 12 Xinghao Chen, et al. FIS920010060US1



collapsed stuck-at faults

non-collapsed stuck-at faults

Figure 7

Page 6 of 15 Xinghao Chen, et al. FIS920010060US1

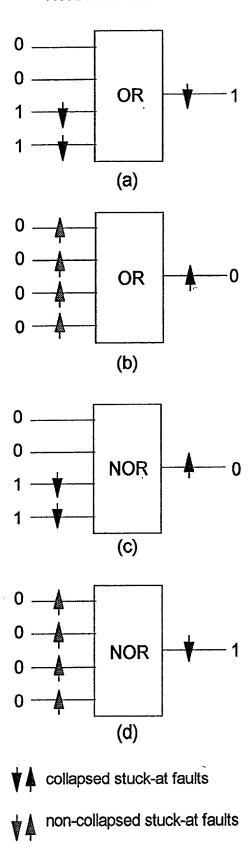


Figure 8.

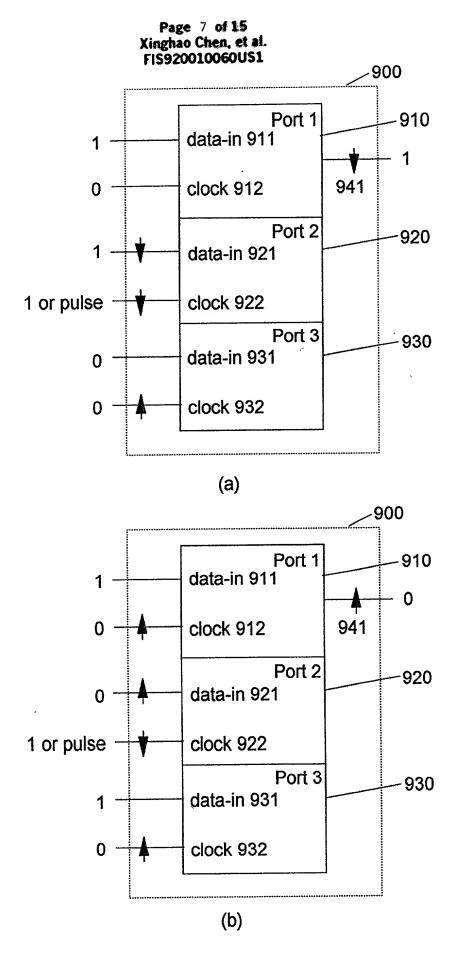
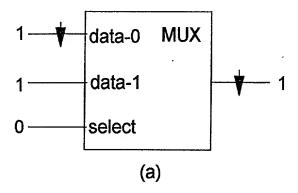
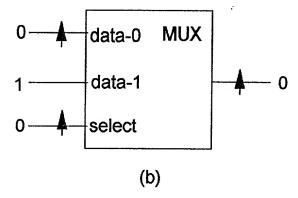


Figure 9.

Page 8 of 15 Xinghao Chen, et al. FIS920010060US1





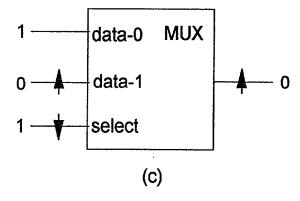
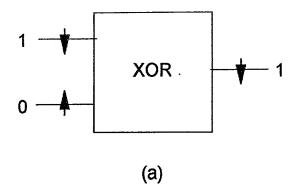
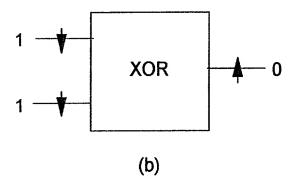


Figure 10.

# Page <sup>9</sup> of 15 Xinghao Chen, et al. FIS920010060US1





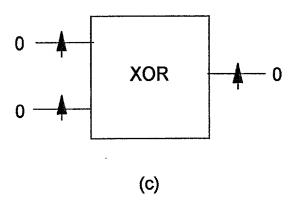
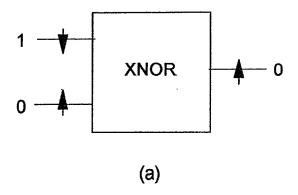
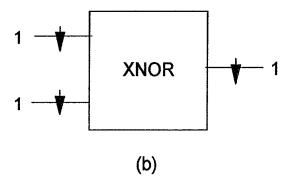


Figure 11.

# Page 10 of 15 Xinghao Chen, et al. FIS920010060US1





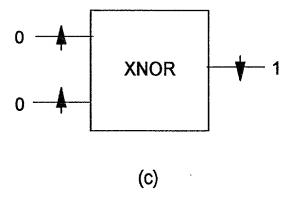


Figure 12.

### Page 11 of 15 Xinghao Chen, et al. FIS920010060US1

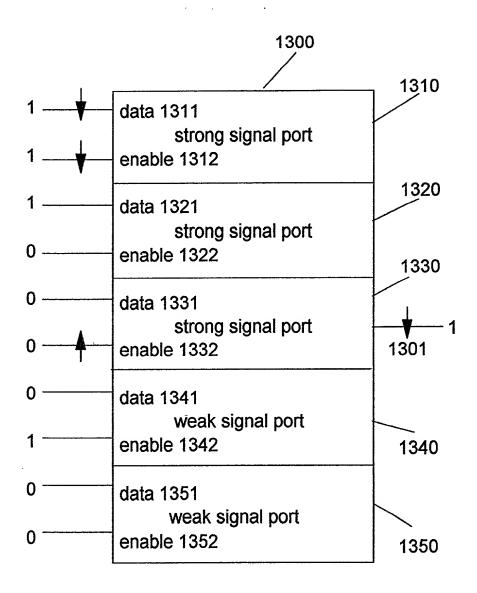


Figure 13.

Page 12 of 15 Xinghao Chen, et al. FIS920010060US1

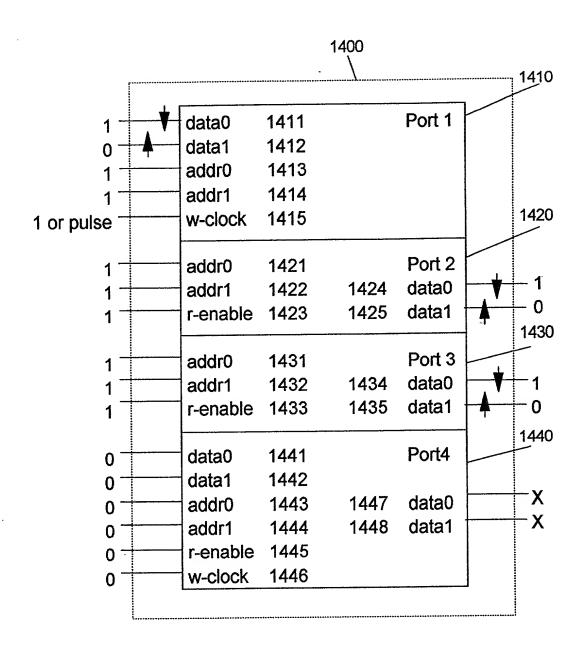


Figure 14.

## Page 13 of 15 Xinghao Chen, et al. FIS920010060US1

ROM	CONTEN	T TABLE	
addr0 addr1		bit0	bit1
0	0	0	0
0	1	0	1
1	0	1	0
1	1	1	1

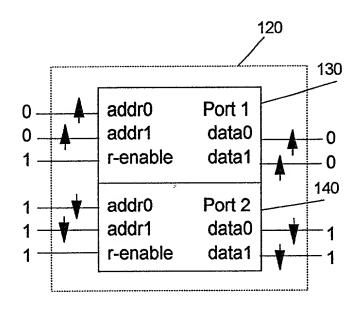


Figure 15.

#### Page 14 of 15 Xinghao Chen, et al. FIS920010060US1

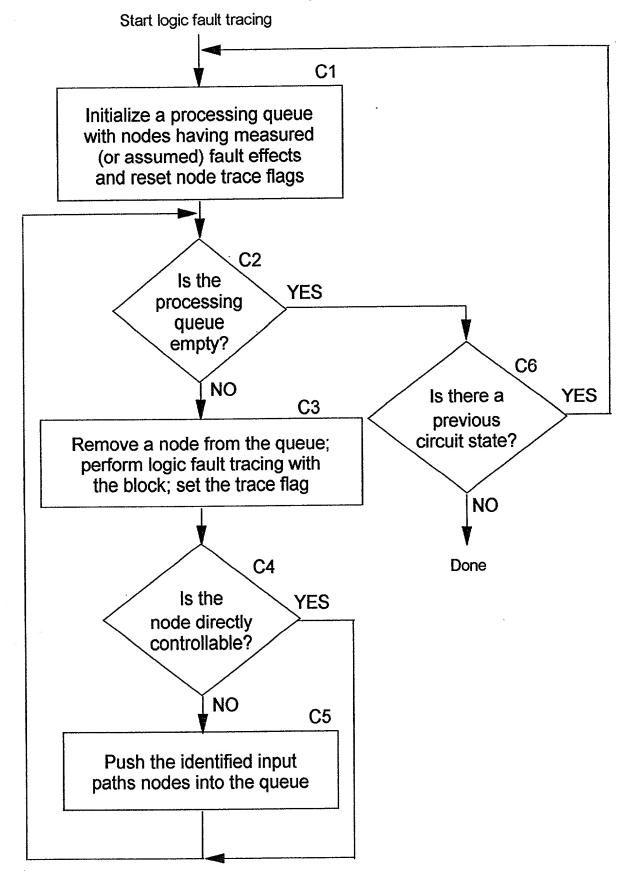


Figure 16.

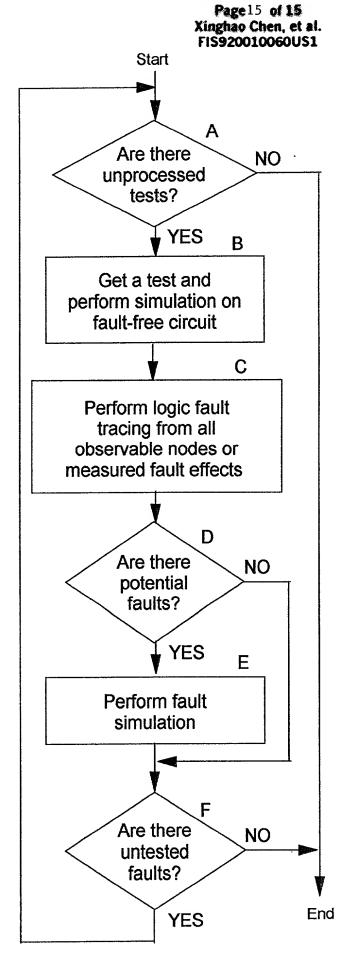


Figure 17.